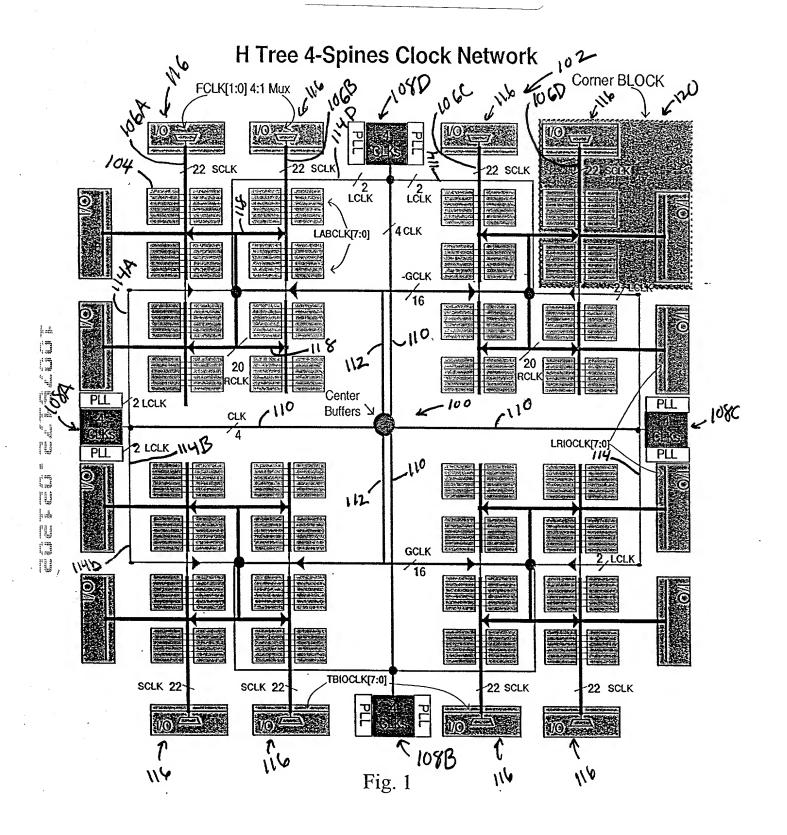
Title: HIGH SPEED PROGRAMMABLE CLOCK

NETWORK

Inventor: Triet NGUYEN, et al. Application No.: To Be Assigned Docket No.: 306812002000

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Title: HIGH SPEED PROGRAMMABLE CLOCK NETWORK

Inventor: Triet NGUYEN, et al. Application No.: To Be Assigned Docket No.: 306812002000

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# H Tree 2-Spines Clock Network

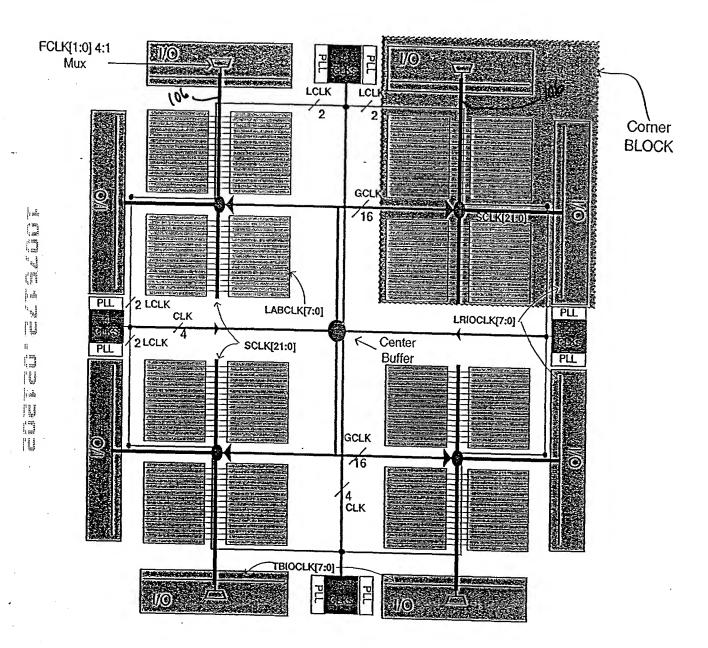


Fig. 2

Title: HIGH SPEED PROGRAMMABLE CLOCK

NETWORK
Inventor: Triet NGUYEN, et al. Application No.: To Be Assigned Docket No.: 306812002000

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### **CORNER BLOCK for 4-Spines Clock Network**

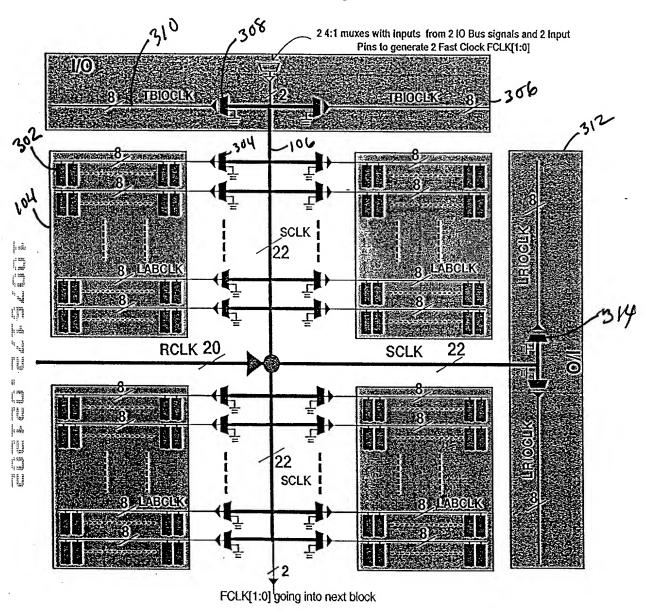


Fig. 3

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# **CORNER BLOCK for 2-Spines Clock Network**

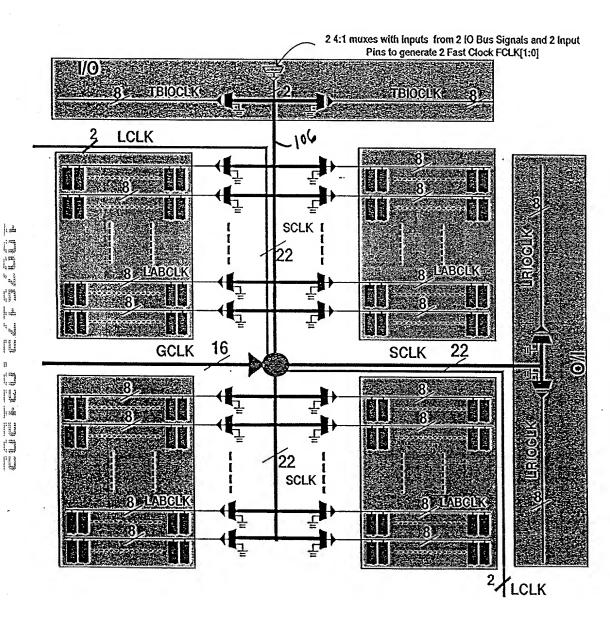


Fig. 4

And the Con-

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NETWORK

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#### **GLOBAL and LOCAL Clocks Generation**

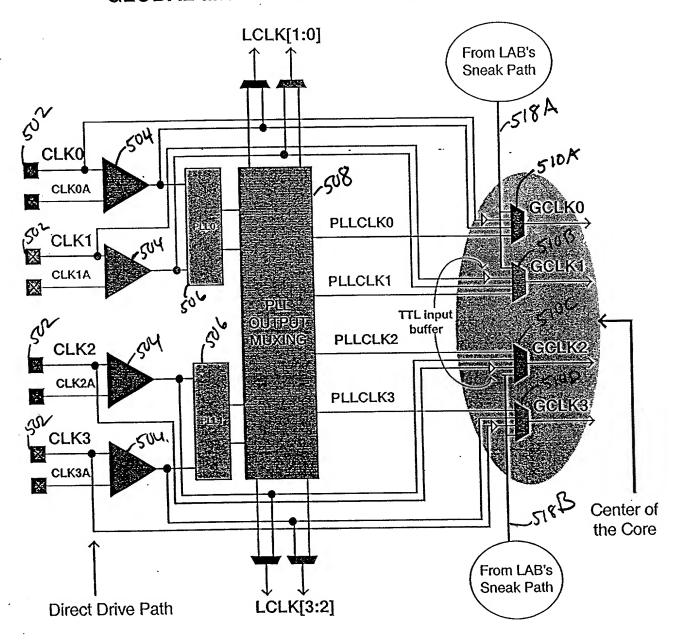


Fig. 5

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### Generation of Global Clocks GCLK[15:0]

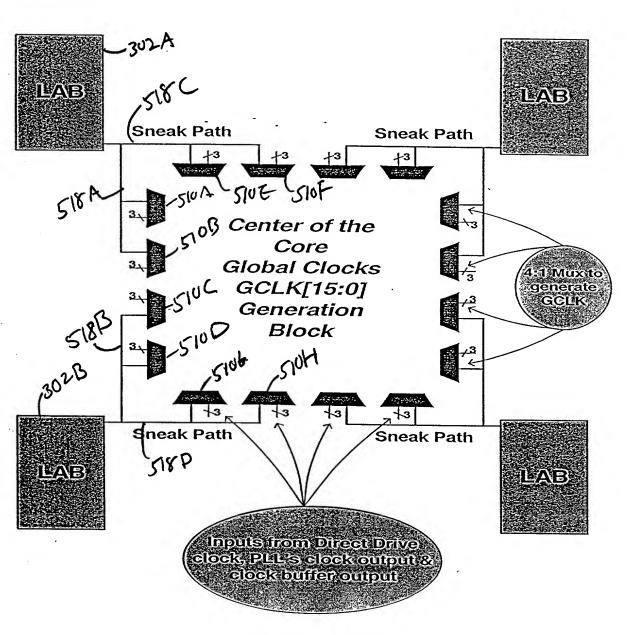


Fig. 6

Title: HIGH SPEED PROGRAMMABLE CLOCK

NETWORK

Inventor: Triet NGUYEN, et al. Application No.: To Be Assigned Docket No.: 306812002000

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## Power Bus Segmentation for CLOCKs

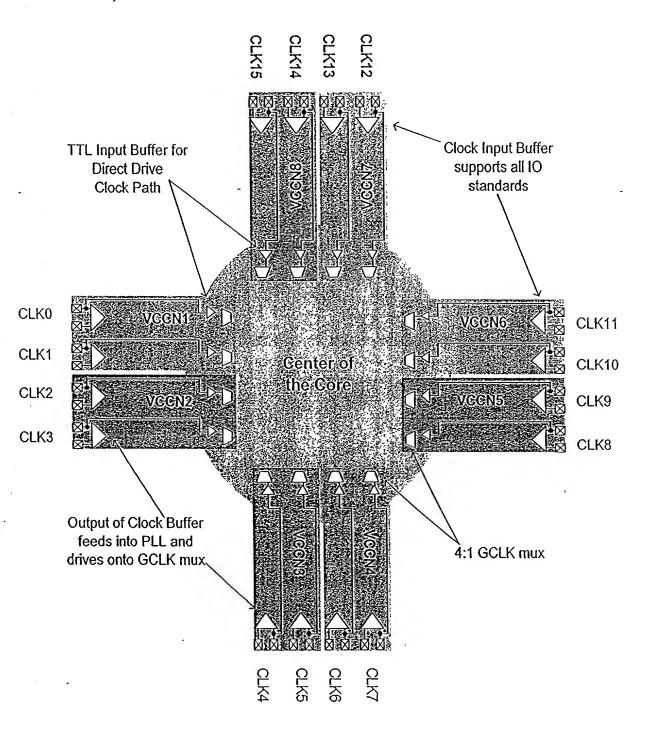


Fig. 7